

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Bernd Deutschmann, et al.                      Art Unit : Unknown  
Serial No. : 10/588,984    Examiner : Unknown  
Filed : November 21, 2006    Conf. No. : 4272  
Title : CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN  
INTEGRATED SEMICONDUCTOR CIRCUIT

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
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**INFORMATION DISCLOSURE STATEMENT**

Please consider the references listed on the enclosed PTO-1449 form. Foreign patent documents and non-patent literature are enclosed; cited U.S. patents and patent application publications will be provided on request.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office Action on the merits. Apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

October 27, 2008  
Date: \_\_\_\_\_

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I hereby certify that this paper was filed with the Patent and Trademark Office using the EFS-WEB system on this date: October 27, 2008